· · · · · · · · · · · · · · · · · · ·	Application No.	Application No. Applicant(s)	
Notice of Allowability	10/045,651	GANOT ET AL.	
	Examiner	Art Unit	\
	Christopher L. Lavin	2624	,
The MAILING DATE of this communication ap All claims being allowable, PROSECUTION ON THE MERITS herewith (or previously mailed), a Notice of Allowance (PTOL-8 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT of the Office or upon petition by the applicant. See 37 CFR 1.3	IS (OR REMAINS) CLOSED in 35) or other appropriate communication is supplication is supplication is supplication is supplication.	this application. If not included nication will be mailed in due cou	rse. THIS
1. This communication is responsive to <u>05/03/07</u> .	•		
2. The allowed claim(s) is/are 16 - 18 and 20 - 24.			
 3. Acknowledgment is made of a claim for foreign priority a) All b) Some⁺ c) None of the: 1. Certified copies of the priority documents had 2. Certified copies of the priority documents had 3. Copies of the certified copies of the priority 	ave been received. ave been received in Application	n No	from the
International Bureau (PCT Rule 17.2(a)).		•	
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATI noted below. Failure to timely comply will result in ABANDON THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	NMENT of this application.		
4. A SUBSTITUTE OATH OR DECLARATION must be sub- INFORMAL PATENT APPLICATION (PTO-152) which g			ICE OF
5. CORRECTED DRAWINGS (as "replacement sheets") m	nust be submitted.		
(a) I including changes required by the Notice of Draftsport	erson's Patent Drawing Review	(PTO-948) attached	
1) 🔲 hereto or 2) 🔲 to Paper No./Mail Date	·		
(b) ☐ including changes required by the attached Examine Paper No./Mail Date	er's Amendment / Comment or	in the Office action of	
Identifying indicia such as the application number (see 37 CFF each sheet. Replacement sheet(s) should be labeled as such i			ck) of
6. DEPOSIT OF and/or INFORMATION about the depattached Examiner's comment regarding REQUIREMEN			e the .
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Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948)	3) 6. 🗌 Interview Su	ormal Patent Application mmary (PTO-413),	
3. ⊠ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 05/03/07	Paper No./N 7. ☐ Examiner's A	Mail Date Amendment/Comment	
4. Examiner's Comment Regarding Requirement for Deposition of Biological Material	it 8. ⊠ Examiner's \$	Statement of Reasons for Allowar	nce

Art Unit: 2624

DETAILED ACTION

1. This action is in response to the RCE filed on 05/03/07.

Information Disclosure Statement

2. The information disclosure statement (IDS) submitted on 05/03/07 was filed after the mailing date of the notice for allowance on 03/09/07. The submission is in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is being considered by the examiner. As the IDS was the reason for filing the RCE the examiner will comment on the more relevant pieces of prior art. Levien (5,402,726) does his operations with the two circuit boards apart, and therefore does not read on the claims. The most relevant art is the Japanese cases (09-172255, 10-084184, and 10-41636) all of these applications deal with a similar problem to the current application, aligning circuit patterns on upper boards that are already attached to lower boards. The three reference differ from the current application in that they use reference marks on the lower board. The current application uses circuit patterns on the lower board. Therefore the three reference do not teach the concept of the current claims.

Allowable Subject Matter

- 3. Claims 16 18 and 20 24 are allowed.
- 4. The following is an examiner's statement of reasons for allowance:

The art of record does not teach nor does it suggest the specific features called for in the claims. The claims call for aligning a pattern on an upper layer with respect to at least one hole, which is drilled into the upper layer, but not through the lower layer

Art Unit: 2624

while the two layers are attached. The concept of aligning a pattern on the upper layer based on holes, either going through the lower layer or not, is not of and by itself new. Combining this concept with the idea of placing the pattern based on a predetermined alignment with the circuitry on the lower board in relationship to the holes overcomes the prior art.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Christopher L. Lavin whose telephone number is 571-272-7392. The examiner can normally be reached on M - F (8:30 - 5:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bhavesh M. Mehta can be reached on (571) 272-7453. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Application/Control Number: 10/045,651

Art Unit: 2624

Page 4

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Christopher Lavin

Brian P. Werner
Supervisory Patent Examiner

Art Unit 2624